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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10020568	FILING DATE 12/06/2001	CLASS 716	SUBCLASS 188	GAU 2325	EXAMINER Suar ez
<b>**APPLICANTS:</b> Orenstien Doron; Ronen Ronny; 702 2857					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 113 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 42390P10915	
Verified and Acknowledged Examiner's initials					
TITLE : Distribution of processing activity across processing hardware based on power consumption considerations					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims 37	Print Claim for O.G 1
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg. 7	Figs. Drwg. 7
		Print Fig. 1	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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